

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	268	(splitter\$1 and (board\$1 substrate\$1 printed module\$1) and (reverse\$1 opposite)).clm.	US-PGPUB	OR	OFF	2006/03/11 15:05
L2	431	((analog analogue) and (board\$1 substrate\$1 printed module\$1) and (decode\$2 decoding)).clm.	US-PGPUB	OR	OFF	2006/03/11 15:08
L3	24	((analog analogue) and (board\$1 substrate\$1 printed module\$1) and (decode\$2 decoding) and (reverse\$1 opposite)).clm.	US-PGPUB	OR	OFF	2006/03/11 15:11
L4	407	2 not 3	US-PGPUB	OR	OFF	2006/03/11 15:11
L5	80	4 and (position\$3 opposing).clm.	US-PGPUB	OR	OFF	2006/03/11 15:14
L6	327	4 not 5	US-PGPUB	OR	OFF	2006/03/11 15:14
L7	0	6 and parallelepiped.clm.	US-PGPUB	OR	OFF	2006/03/11 15:15
L8	13	6 and (interference emi rfi crosstalk\$3 (cross adj talk\$3)).clm.	US-PGPUB	OR	OFF	2006/03/11 15:17
L9	314	6 not 8	US-PGPUB	OR	OFF	2006/03/11 15:17

Interference Search History



## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	583	(361/729,733).ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:14
L2	3	1 and splitter\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 15:55
L3	457	1 and (module\$1 board\$1 card\$1 daughter\$1board\$1 substrate\$1 mother\$1board\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:15
L4	315	3 and (reverse\$1 opposite opposing)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 15:58
L5	65	4 and (emi rfi interference crosstalk\$3 (cross\$1talk\$3))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:01
L6	250	4 not 5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:09
L7	142	3 not 4	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:12
L8	126	1 not 3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:12
L9	0	8 and (analog analogue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:13
L10	1	8 and decod\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/11 16:13
L11	34	(361/729,733).ccls.	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/11 16:14
L12	0	11 and splitter\$1	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/11 16:14
L13	19	11 and (module\$1 board\$1 card\$1 daughter\$1board\$1 substrate\$1 mother\$1board\$1)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/11 16:16
L14	15	11 not 13	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/11 16:17